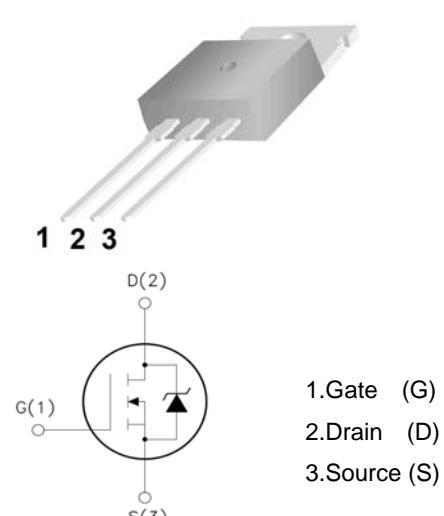




<p><b>Features:</b></p> <ul style="list-style-type: none"><li><input type="checkbox"/> Low Intrinsic Capacitances.</li><li><input type="checkbox"/> Excellent Switching Characteristics.</li><li><input type="checkbox"/> Extended Safe Operating Area.</li><li><input type="checkbox"/> Unrivalled Gate Charge :<math>Q_g = 75\text{nC}</math> (Typ.).</li><li><input type="checkbox"/> <math>V_{DSS}=150\text{V}, I_D=100\text{A}</math></li><li><input type="checkbox"/> <math>R_{DS(on)} : 13\text{m}\Omega</math> (Max) @ <math>V_G=10\text{V}</math></li><li><input type="checkbox"/> 100% Avalanche Tested</li></ul>	<p>TO-220</p> <p></p>  <p>1.Gate (G) 2.Drain (D) 3.Source (S)</p>
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**Absolute Maximum Ratings** ( $T_a=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Value	Unit
$V_{DSS}$	Drain-Source Voltage	150	V
$I_D$	Drain Current	$T_j=25^\circ\text{C}$	100
		$T_j=100^\circ\text{C}$	65
$V_{GSS}$	Gate Threshold Voltage	$\pm 20$	V
$E_{AS}$	Single Pulse Avalanche Energy (note1)	225	mJ
$I_{AR}$	Avalanche Current (note2)	100	A
$P_D$	Power Dissipation ( $T_j=25^\circ\text{C}$ )	230	W
$T_j$	Junction Temperature(Max)	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~+150	
$T_L$	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	300	

**Thermal Characteristics**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	-	0.65	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	-	62.5	



# HGS100N15D150V N-Channel MOSFET

## Electrical Characteristics (Ta=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	150	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(Tc=25°C)	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	3.0	4.0	5.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance③	V <sub>GS</sub> =12V, I <sub>D</sub> =30A	--	11.0	12.5	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance③	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	--	11.2	13.0	mΩ
<b>Dynamic Electrical Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz	--	5960	--	pF
C <sub>oss</sub>	Output Capacitance		--	485	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	125	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =75V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	75	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	26	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	20	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =75V, I <sub>D</sub> =10A, R <sub>G</sub> =6.8Ω, V <sub>GS</sub> =10V	--	22	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	28	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	35	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	12	--	nS
<b>Source- Drain Diode Characteristics</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =30A, V <sub>GS</sub> =0V	--	0.80	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>j</sub> =25°C, I <sub>sd</sub> =20A, V <sub>GS</sub> =0V di/dt=300A/μs	--	50	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge		--	105	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by T<sub>jmax</sub>, starting T<sub>j</sub> = 25°C, L = 0.5mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 30A, V<sub>GS</sub> = 10V. Part not recommended for use above this value
- ③ Pulse width ≤ 300μs; duty cycle≤ 2%.



## Typical Characteristics

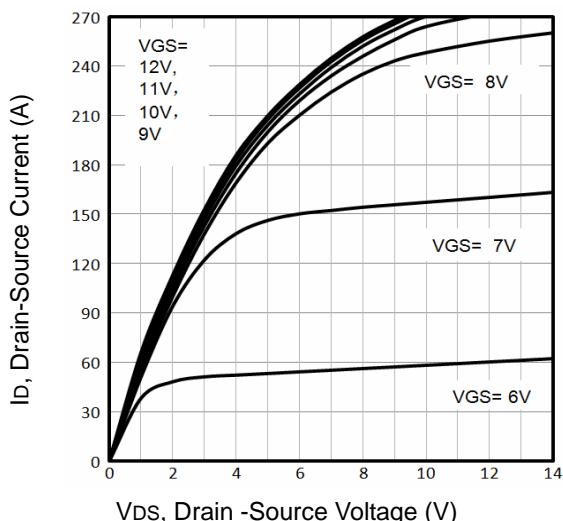


Fig1. Typical Output Characteristics

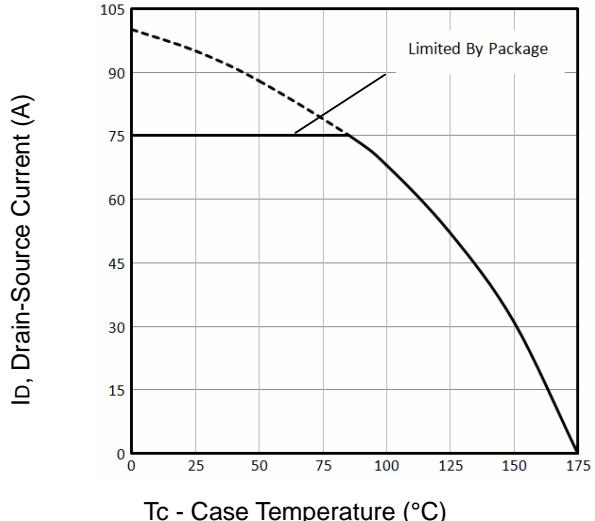


Fig2. Maximum Drain Current Vs Case Temperature

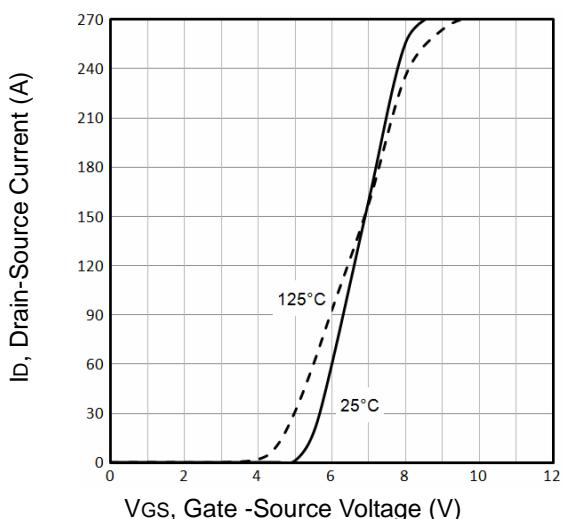


Fig3. Typical Transfer Characteristics

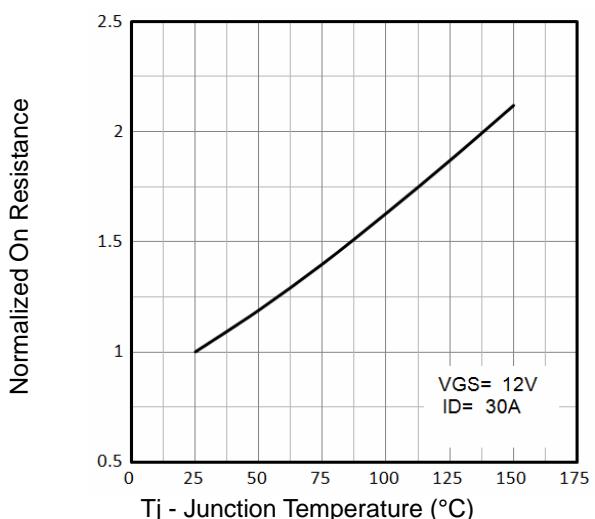


Fig4. Normalized On-Resistance Vs. Temperature

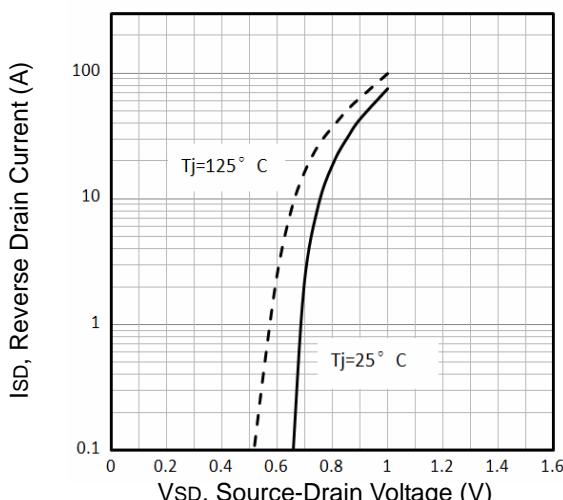


Fig5. Typical Source-Drain Diode Forward Voltage

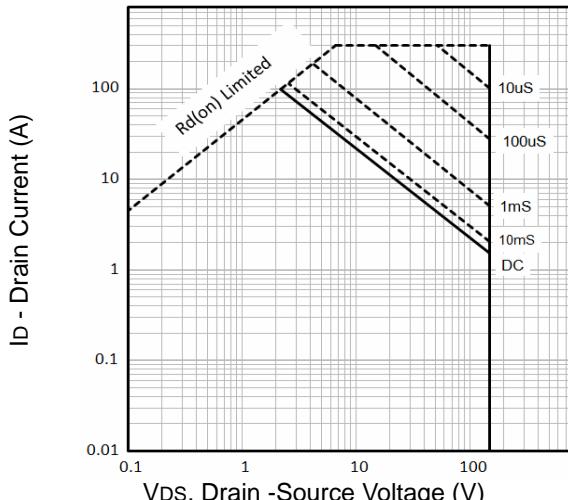


Fig6. Maximum Safe Operating Area



## Typical Characteristics

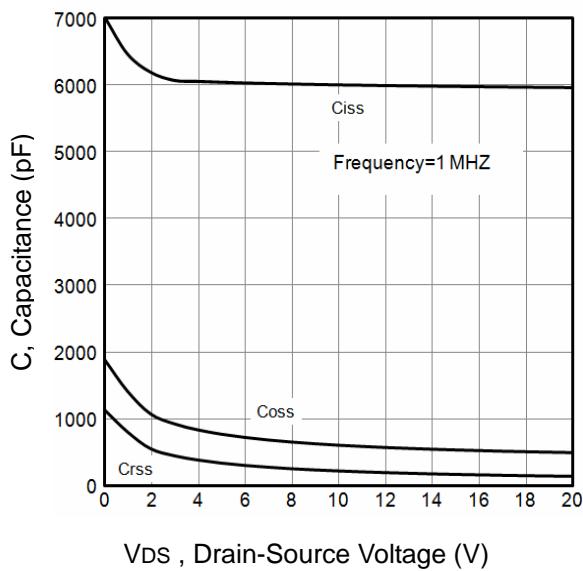


Fig7. Typical Capacitance Vs.Drain-Source Voltage

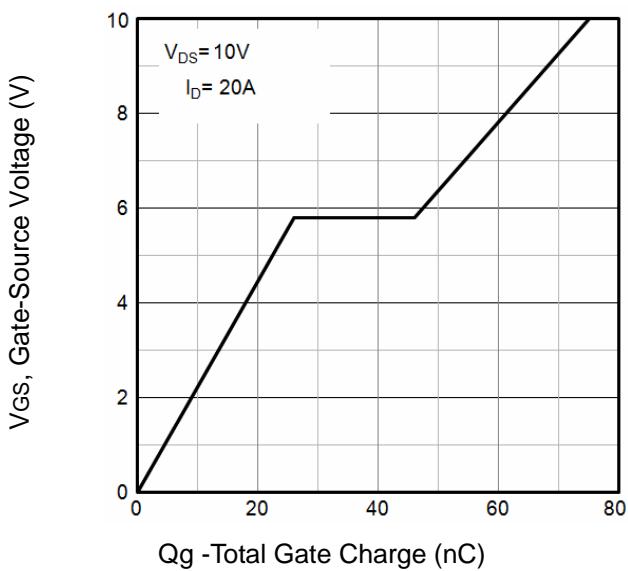


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

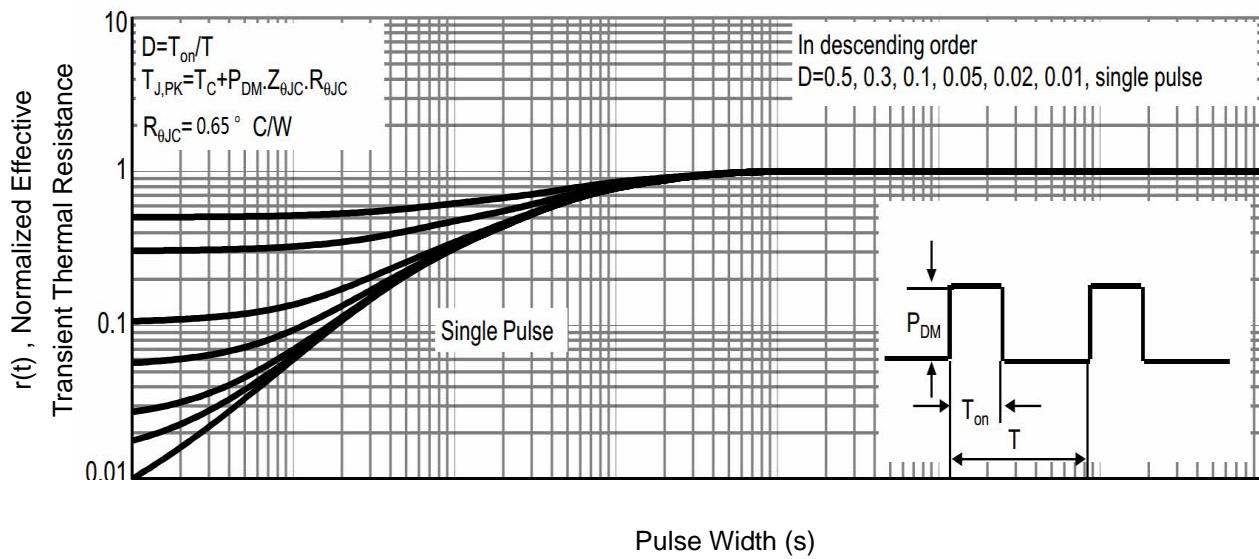


Fig9. Normalized Maximum Transient Thermal Impedance

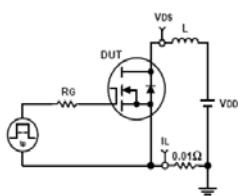


Fig10. Unclamped Inductive Test Circuit and waveforms

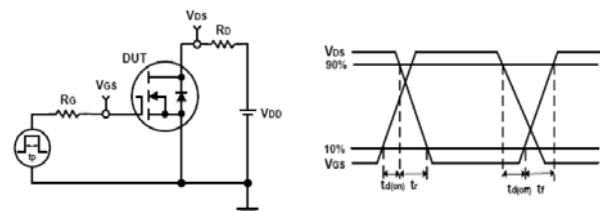
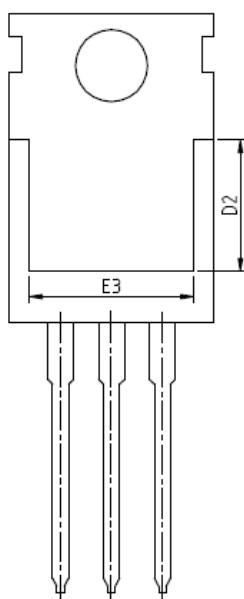
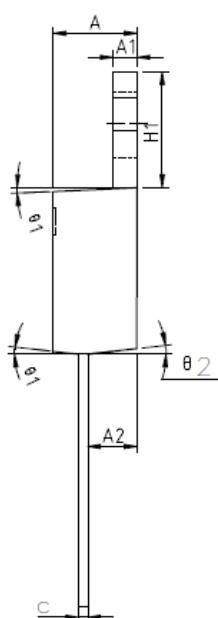
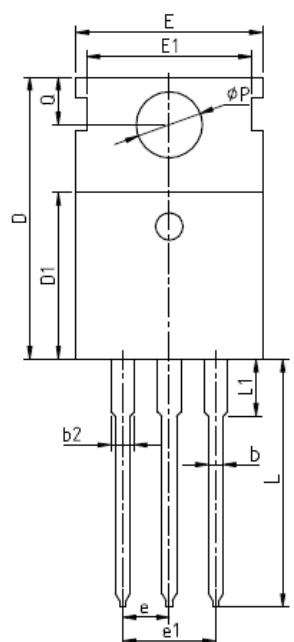


Fig11. Switching Time Test Circuit and waveforms



## Package Dimension

TO-220



SYMBOL	MIN	NOM	MAX
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
c	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.65	10.00	10.35
E3	7.00	8.00	8.40
e	2.54	BSC	
e1	5.08	BSC	
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
phi_P	3.45	3.60	3.75
Q	2.60	2.80	3.00
theta_1	4°	7°	10°
theta_2	0°	3°	6°