

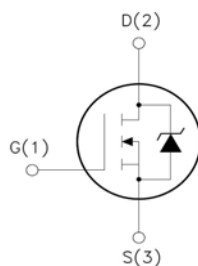
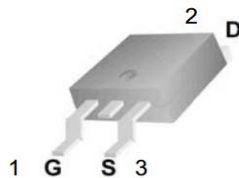


# HGS50N06R 60V N-Channel MOSFET

## Features:

- ☐ Low Intrinsic Capacitances.
- ☐ Excellent Switching Characteristics.
- ☐ Extended Safe Operating Area.
- ☐ Unrivalled Gate Charge :Qg= 50nC
- ☐ (Typ.).BVDSS=60V,I<sub>D</sub>=50A
- ☐ R<sub>DS(on)</sub> : 0.02Ω (Max) @V<sub>G</sub>=10V
- ☐ 100% Avalanche Tested

TO-252



- 1.Gate (G)
- 2.Drain (D)
- 3.Source (S)

## Absolute Maximum Ratings (T<sub>C</sub>=25°C unless otherwise noted)

| Parameter                                         | Symbol                           | Limit      | Unit |
|---------------------------------------------------|----------------------------------|------------|------|
| Drain-Source Voltage                              | V <sub>DSS</sub>                 | 60         | V    |
| Gate-Source Voltage                               | V <sub>GSS</sub>                 | ±20        | V    |
| Drain Current-Continuous                          | I <sub>D</sub>                   | 50         | A    |
| Drain Current-Continuous(T <sub>C</sub> =100°C)   | I <sub>D</sub> (100°C)           | 35.4       | A    |
| Pulsed Drain Current                              | I <sub>DM</sub>                  | 200        | A    |
| Maximum Power Dissipation                         | P <sub>D</sub>                   | 85         | W    |
| Derating factor                                   |                                  | 0.57       | W/°C |
| Single pulse avalanche energy <sup>(Note 5)</sup> | E <sub>AS</sub>                  | 300        | mJ   |
| Operating Junction and Storage Temperature Range  | T <sub>J</sub> ,T <sub>STG</sub> | -55 To 175 | °C   |

## Thermal Characteristic

|                                                         |                  |     |      |
|---------------------------------------------------------|------------------|-----|------|
| Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup> | R <sub>θJC</sub> | 1.8 | °C/W |
|---------------------------------------------------------|------------------|-----|------|



# HGS50N06R 60V N-Channel MOSFET

## Electrical Characteristics ( $T_C=25^{\circ}\text{C}$ unless otherwise noted)

| Parameter                                     | Symbol              | Condition                                                                              | Min | Typ  | Max  | Unit |
|-----------------------------------------------|---------------------|----------------------------------------------------------------------------------------|-----|------|------|------|
| Off Characteristics                           |                     |                                                                                        |     |      |      |      |
| Drain-Source Breakdown Voltage                | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V I <sub>D</sub> =250μA                                              | 60  | -    | -    | V    |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>    | V <sub>DS</sub> =60V, V <sub>GS</sub> =0V                                              | -   | -    | 1    | μA   |
| Gate-Body Leakage Current                     | I <sub>GSS</sub>    | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V                                             | -   | -    | ±100 | nA   |
| On Characteristics <sup>(Note 3)</sup>        |                     |                                                                                        |     |      |      |      |
| Gate Threshold Voltage                        | V <sub>GSS</sub>    | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                               | 1.4 | 1.9  | 2.5  | V    |
| Drain-Source On-State Resistance              | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =20A                                              | -   | 14   | 20   | mΩ   |
| Forward Transconductance                      | g <sub>FS</sub>     | V <sub>DS</sub> =5V, I <sub>D</sub> =20A                                               | 18  | -    | -    | S    |
| Dynamic Characteristics <sup>(Note4)</sup>    |                     |                                                                                        |     |      |      |      |
| Input Capacitance                             | C <sub>ISS</sub>    | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V,<br>F=1.0MHz                                 | -   | 2050 | -    | PF   |
| Output Capacitance                            | C <sub>OSS</sub>    |                                                                                        | -   | 158  | -    | PF   |
| Reverse Transfer Capacitance                  | C <sub>RSS</sub>    |                                                                                        | -   | 120  | -    | PF   |
| Switching Characteristics <sup>(Note 4)</sup> |                     |                                                                                        |     |      |      |      |
| Turn-on Delay Time                            | t <sub>d(on)</sub>  | V <sub>DD</sub> =30V, R <sub>L</sub> =6.7Ω<br>V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω | -   | 7.4  | -    | nS   |
| Turn-on Rise Time                             | t <sub>r</sub>      |                                                                                        | -   | 5.1  | -    | nS   |
| Turn-Off Delay Time                           | t <sub>d(off)</sub> |                                                                                        | -   | 28.2 | -    | nS   |
| Turn-Off Fall Time                            | t <sub>f</sub>      |                                                                                        | -   | 5.5  | -    | nS   |
| Total Gate Charge                             | Q <sub>g</sub>      | V <sub>DS</sub> =30V, I <sub>D</sub> =20A,<br>V <sub>GS</sub> =10V                     | -   | 50   | -    | nC   |
| Gate-Source Charge                            | Q <sub>gs</sub>     |                                                                                        | -   | 6    | -    | nC   |
| Gate-Drain Charge                             | Q <sub>gd</sub>     |                                                                                        | -   | 15   | -    | nC   |
| Drain-Source Diode Characteristics            |                     |                                                                                        |     |      |      |      |
| Diode Forward Voltage <sup>(Note 3)</sup>     | V <sub>SD</sub>     | V <sub>GS</sub> =0V, I <sub>S</sub> =20A                                               | -   |      | 1.2  | V    |
| Diode Forward Current <sup>(Note 2)</sup>     | I <sub>S</sub>      |                                                                                        | -   | -    | 50   | A    |
| Reverse Recovery Time                         | t <sub>rr</sub>     | T <sub>J</sub> = 25°C, I <sub>F</sub> =20A<br>di/dt = 100A/μs <sup>(Note3)</sup>       | -   | 28   | -    | nS   |
| Reverse Recovery Charge                       | Q <sub>rr</sub>     |                                                                                        | -   | 40   | -    | nC   |
| Forward Turn-On Time                          | t <sub>on</sub>     | Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)                   |     |      |      |      |

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, L=0.5mH, R_g=25\Omega$



## Typical Characteristics

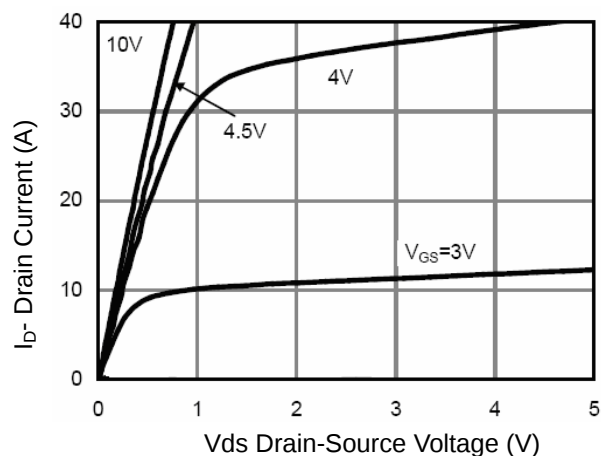


Figure 1 Output Characteristics

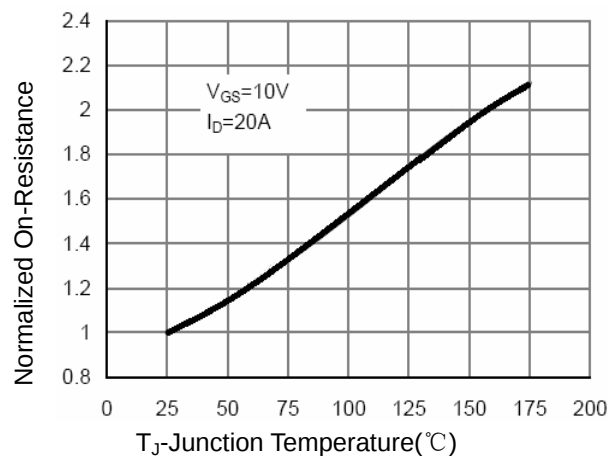


Figure 4 Rdson-Junction Temperature

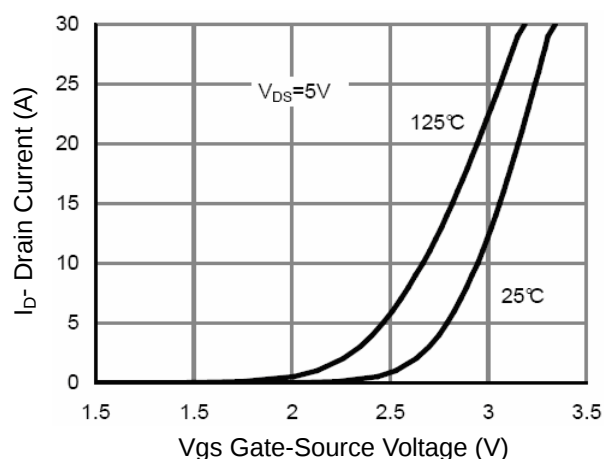


Figure 2 Transfer Characteristics

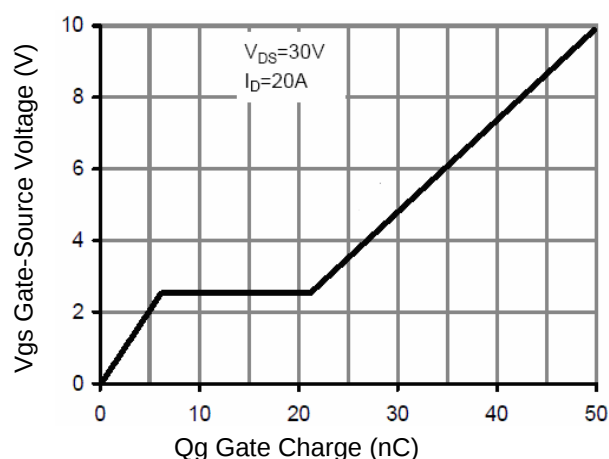


Figure 5 Gate Charge

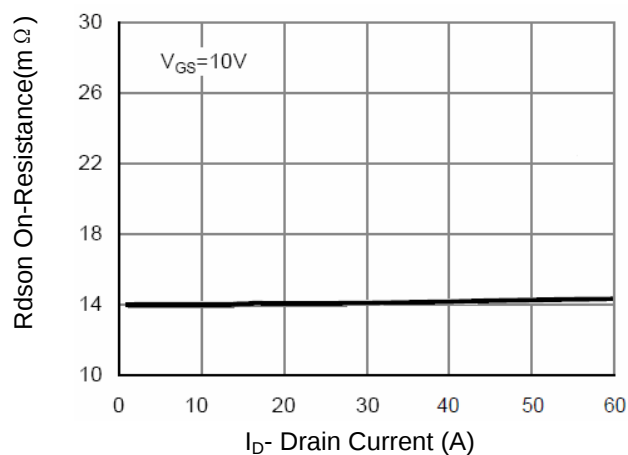


Figure 3 Rdson- Drain Current

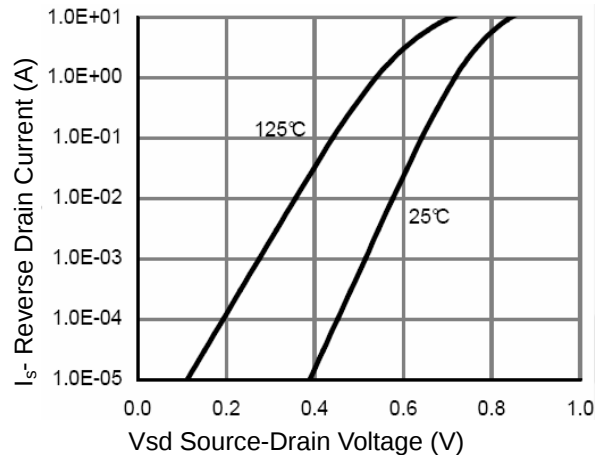


Figure 6 Source- Drain Diode Forward



## Typical Characteristics (Continued)

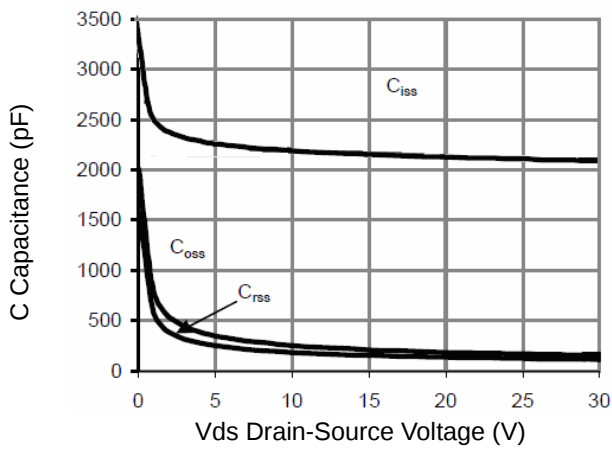


Figure 7 Capacitance vs Vds

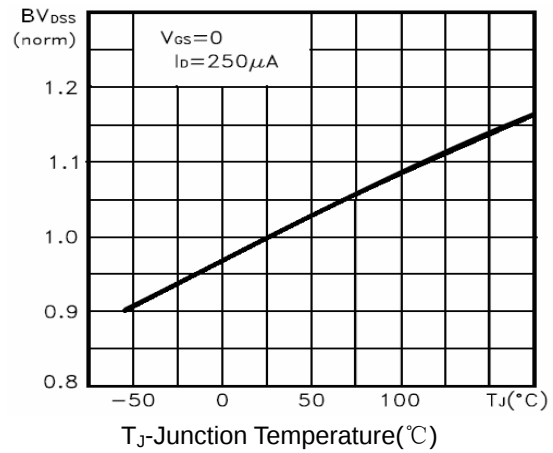


Figure 9  $BV_{DSS}$  vs Junction Temperature

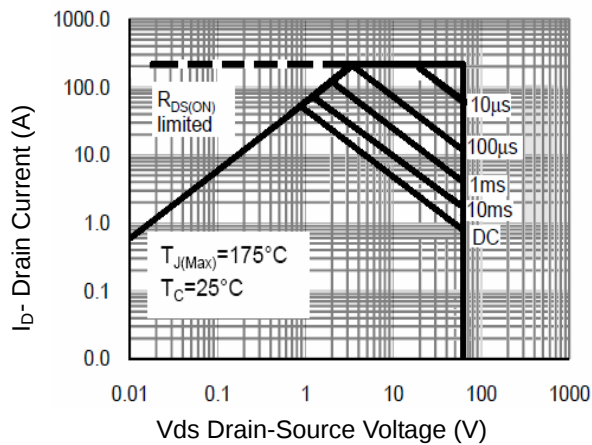


Figure 8 Safe Operation Area

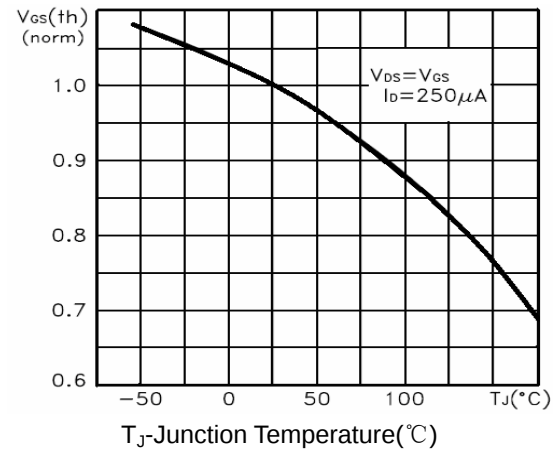


Figure 10  $V_{GS(th)}$  vs Junction Temperature

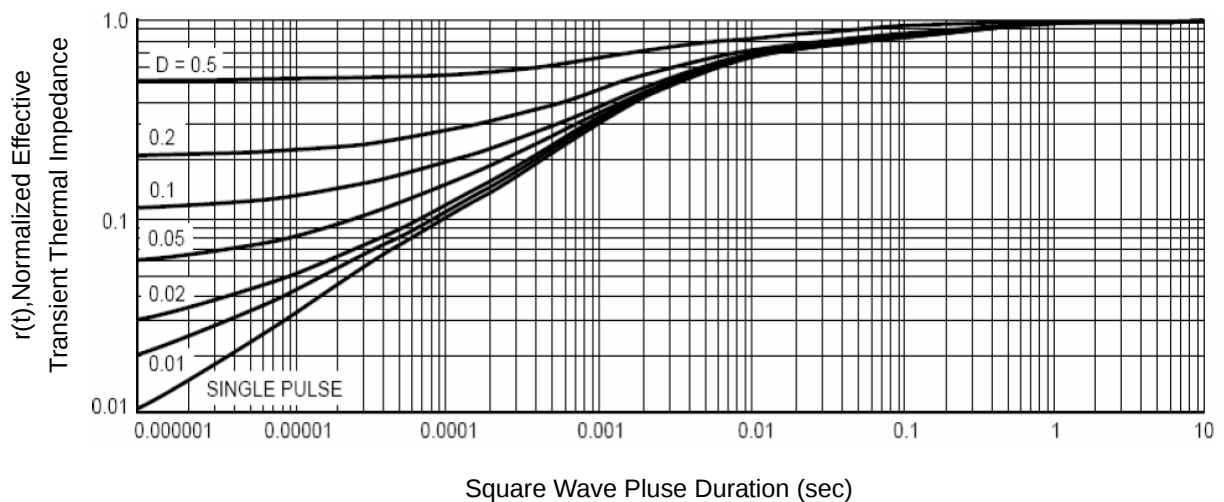
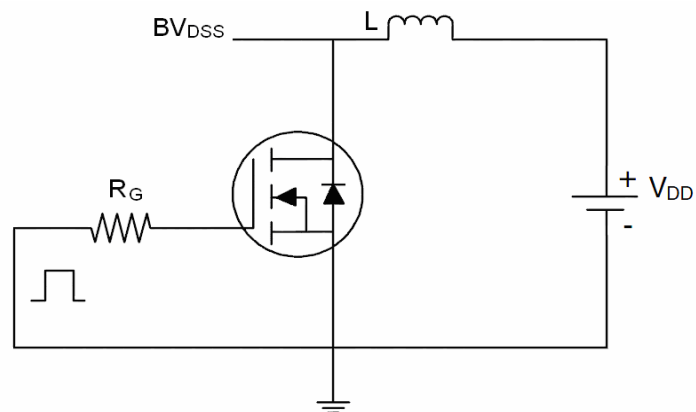


Figure 11 Normalized Maximum Transient Thermal Impedance

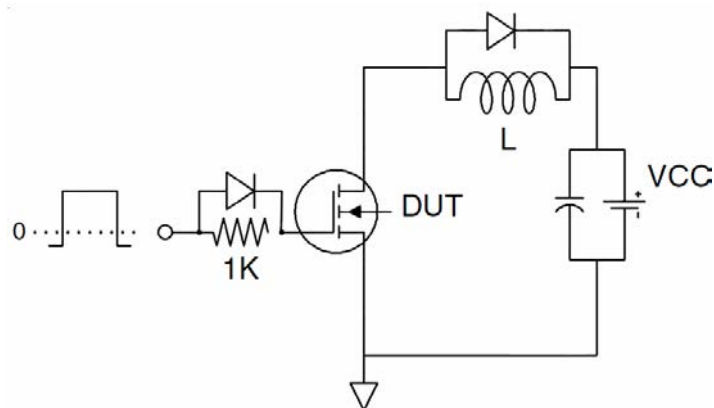


## Test Circuit

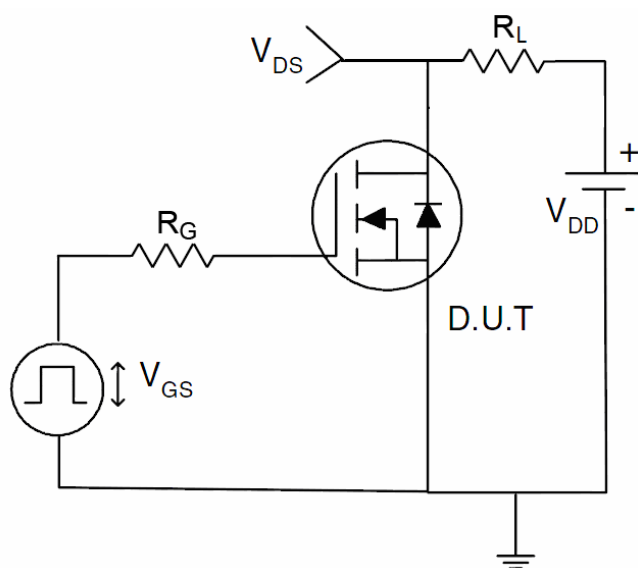
### 1) $A_S$ test Circuit



### 2) Gate charge test Circuit



### 3) Switch Time Test Circuit

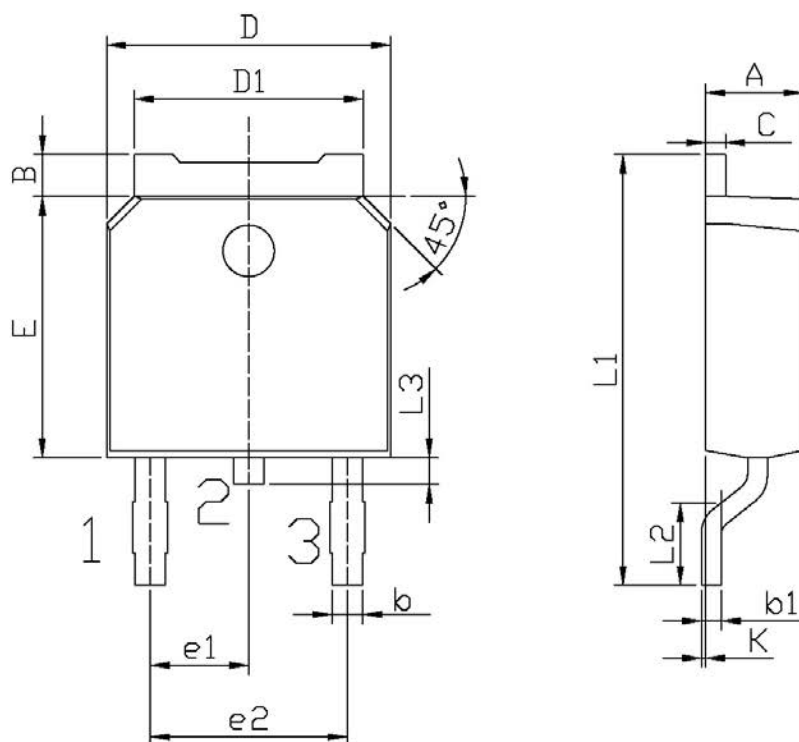




## Package Dimension

TO-252

Unit:mm



| Symbol | Dimensions In Millimeters |      | Symbol | Dimensions In Millimeters |       |
|--------|---------------------------|------|--------|---------------------------|-------|
|        | Min                       | Max  |        | Min                       | Max   |
| A      | 2.20                      | 2.40 | E      | 5.95                      | 6.25  |
| B      | 0.95                      | 1.25 | e1     | 2.24                      | 2.34  |
| b      | 0.70                      | 0.90 | e2     | 4.43                      | 4.73  |
| b1     | 0.45                      | 0.55 | L1     | 9.85                      | 10.35 |
| C      | 0.45                      | 0.55 | L2     | 1.25                      | 1.75  |
| D      | 6.45                      | 6.75 | L3     | 0.60                      | 0.90  |
| D1     | 5.20                      | 5.40 | K      | 0.00                      | 0.10  |